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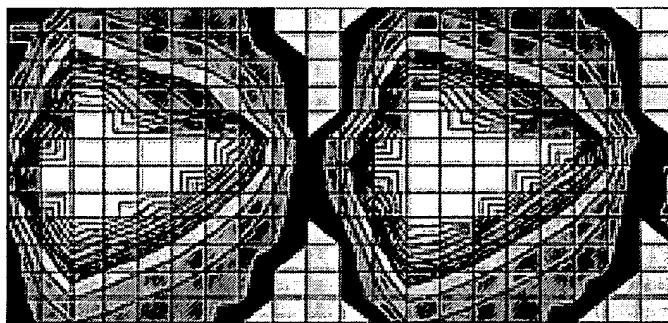


Figure 1a

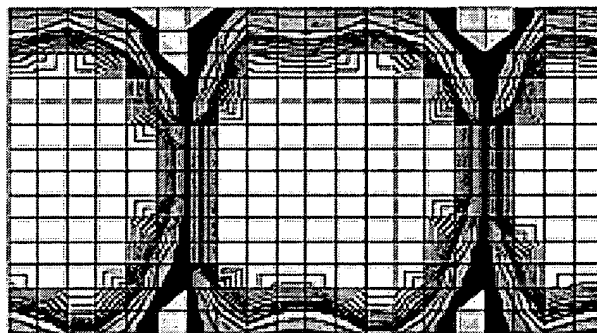


Figure 1b

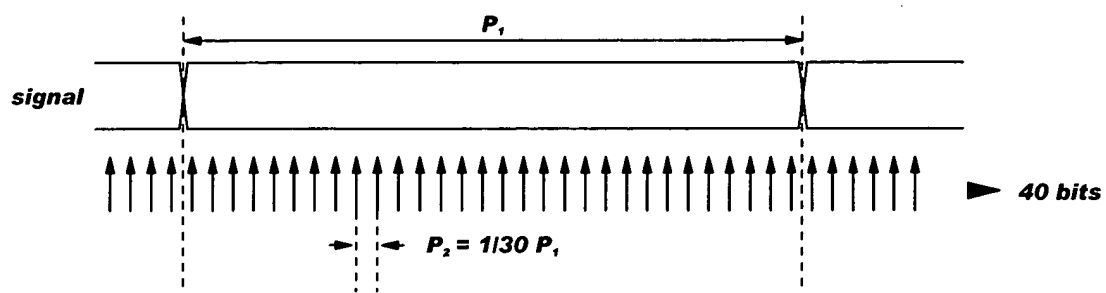


Figure 2

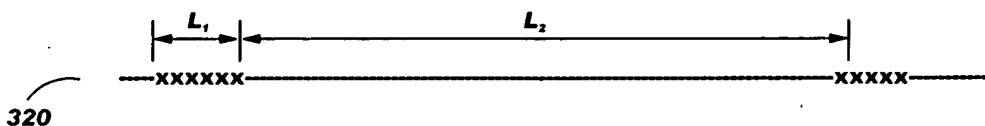
[illegible]

Figure 3

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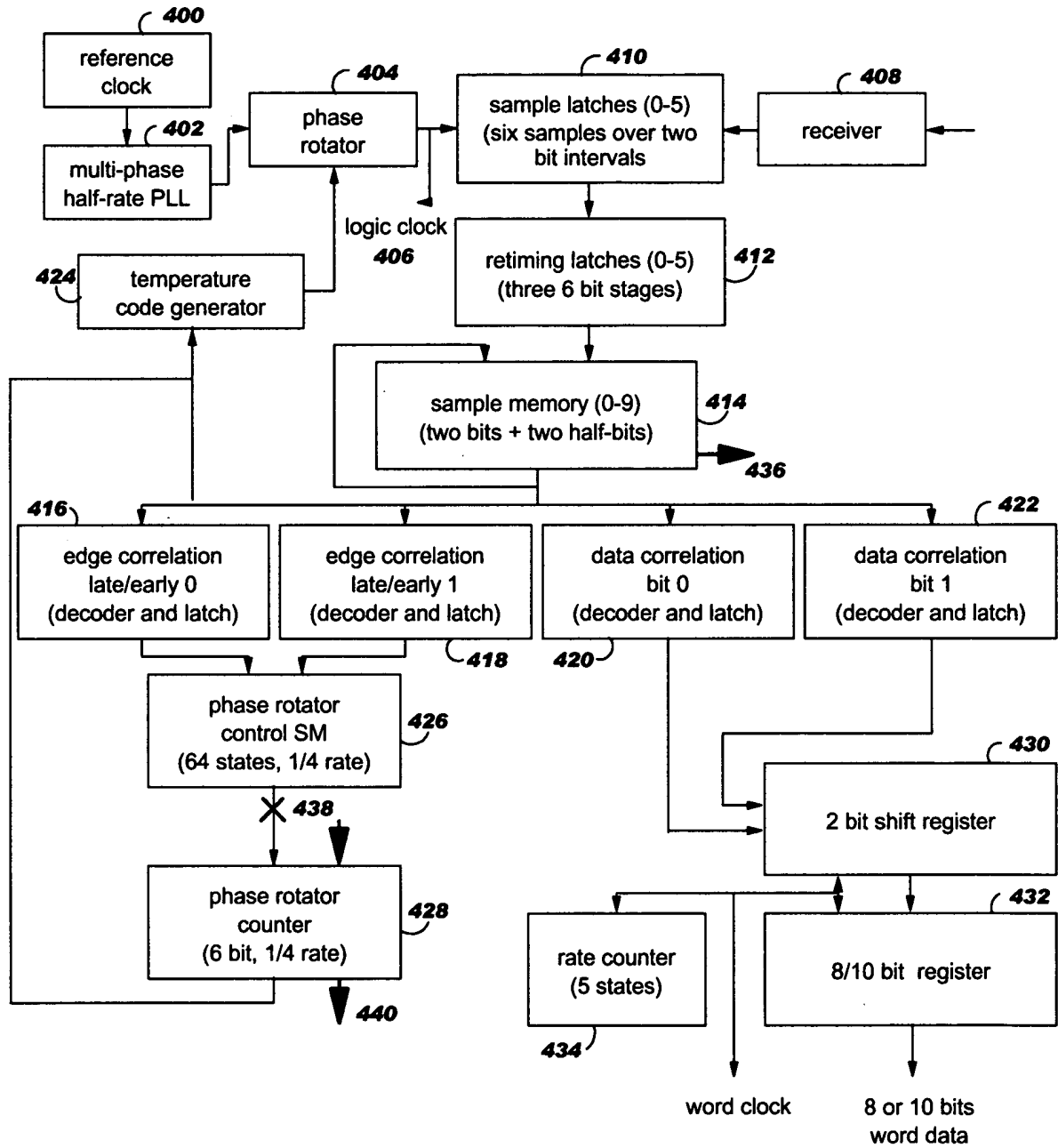


Figure 4

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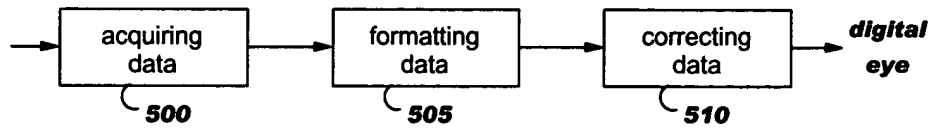


Figure 5

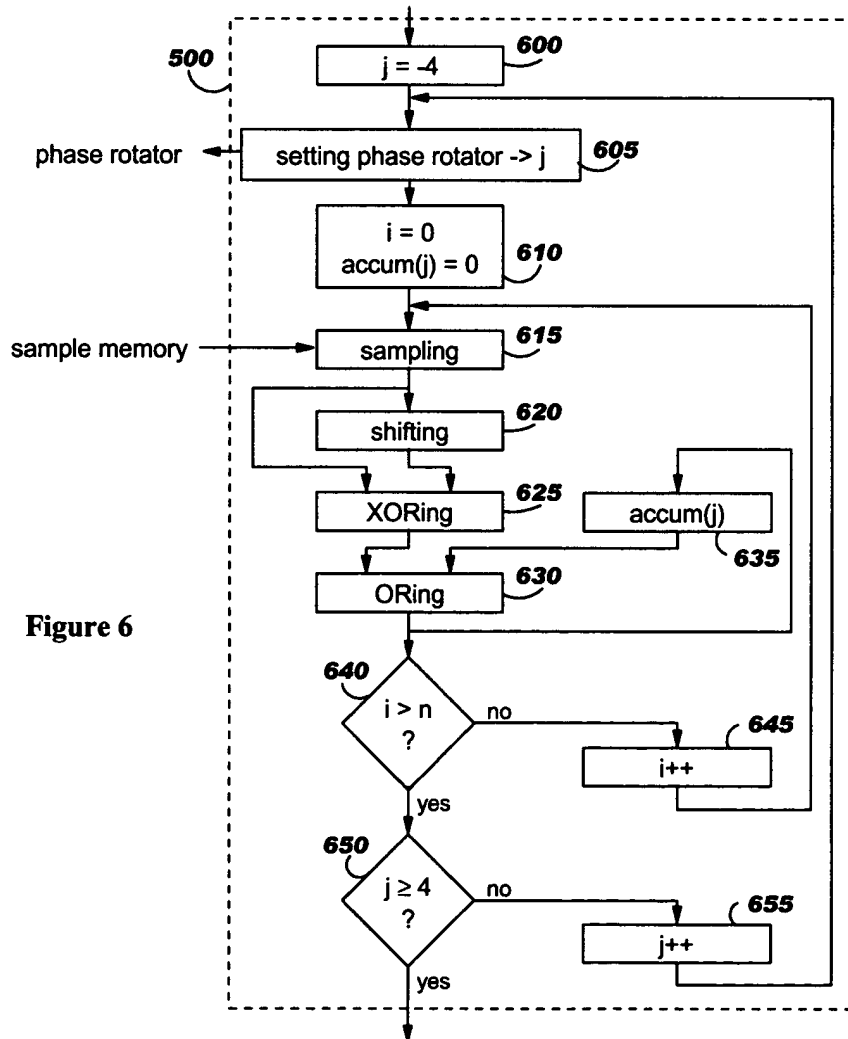


Figure 6

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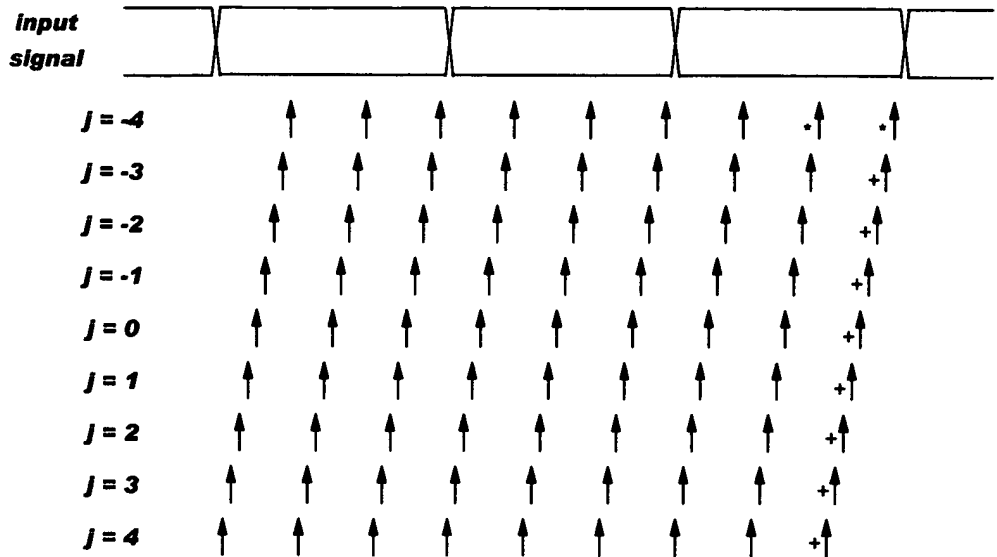
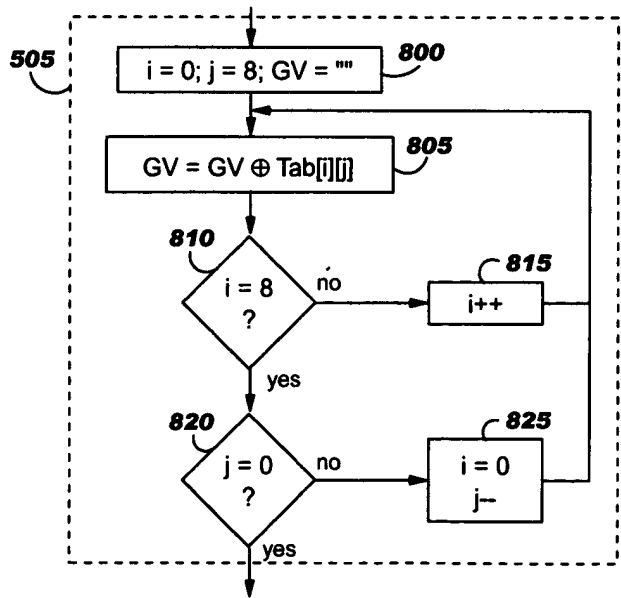


Figure 7a

Pos./Bit	0	1	2	3	4	5	6	7	8
j = -4	1	1	0	0	1	0	1	1	0
j = -3	1	1	0	0	1	0	1	1	0
j = -2	1	1	0	0	1	0	1	1	0
j = -1	1	1	0	0	1	0	1	1	0
j = 0	1	1	0	0	1	0	1	1	0
j = 1	0	1	1	0	1	0	0	1	1
j = 2	0	1	1	0	1	1	0	1	1
j = 3	0	1	1	0	1	1	0	1	1
j = 4	0	1	1	0	1	1	0	1	1

Figure 7b

Figure 8



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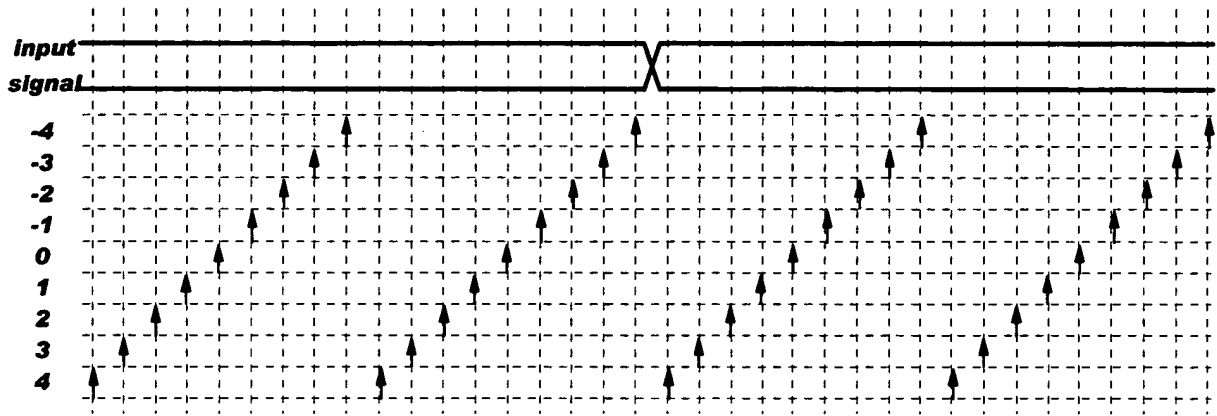


Figure 9

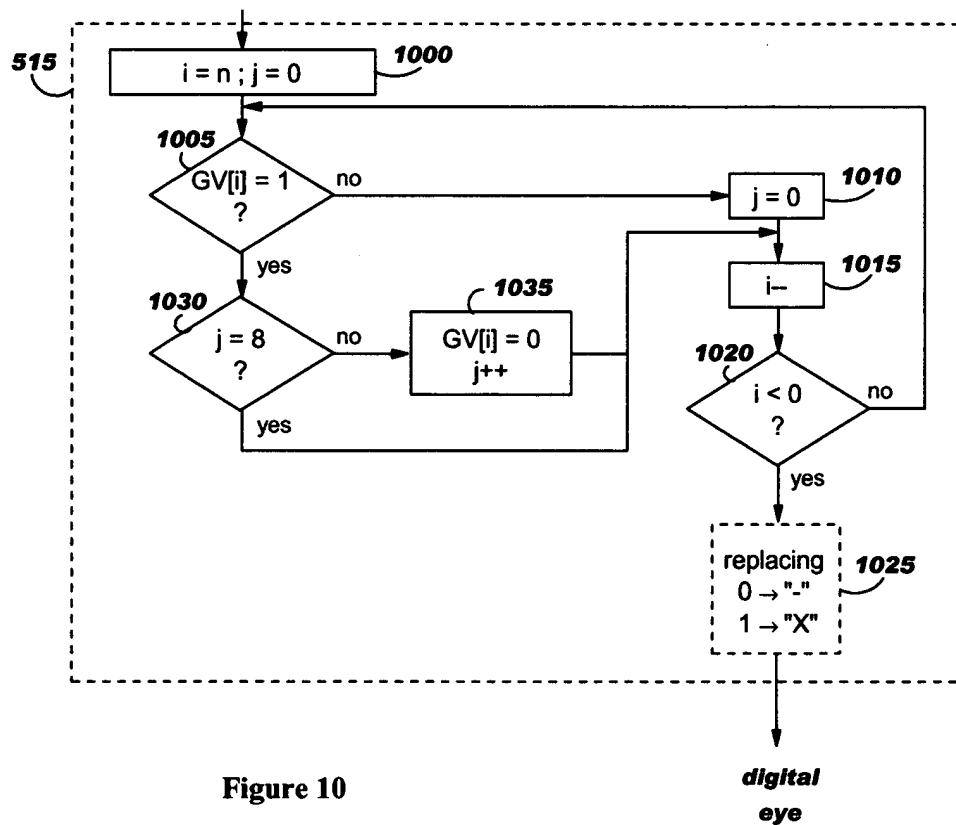


Figure 10

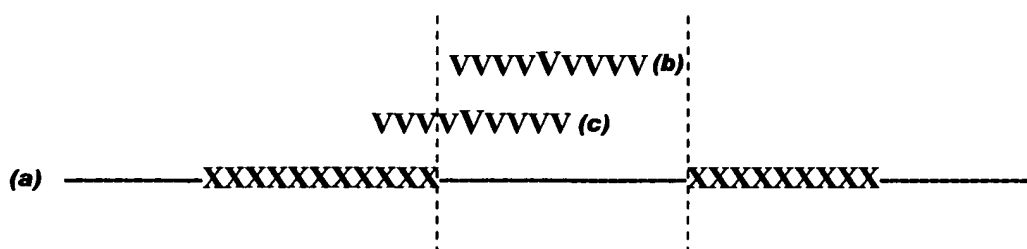


Figure 11

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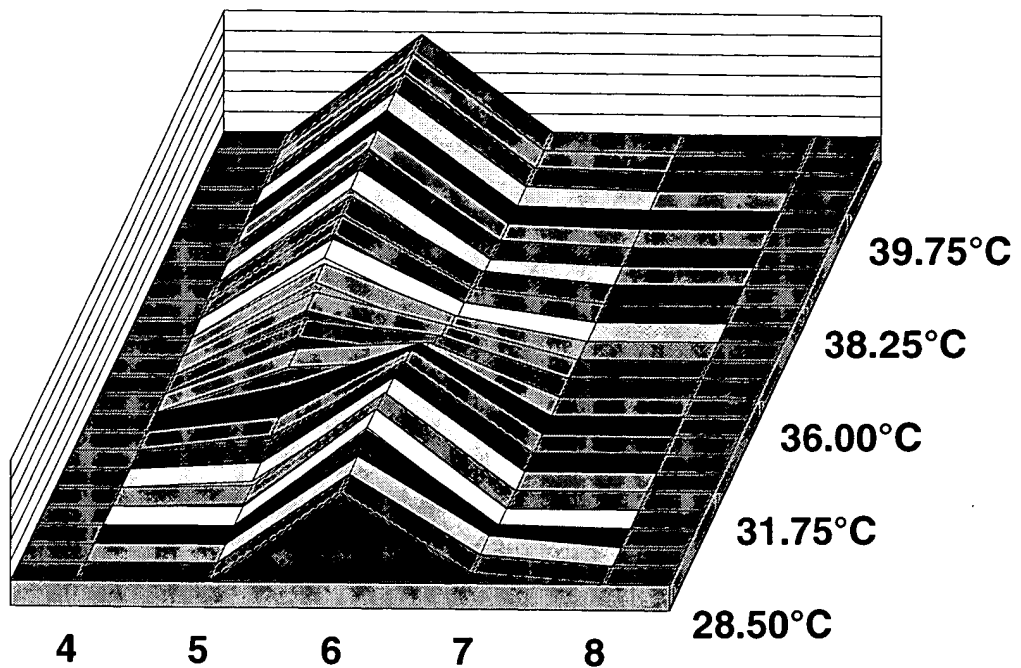


Figure 12